# Current Carrying Capacity of Quasi-1D ZrTe<sub>3</sub> van der Waals Nanoribbons

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Abstract — Quasi-1D van der Waals materials, such as transition metal trichalcogenides, have strong covalent bonds in one direction and weaker bonds in cross-plane directions. They can be prepared as crystalline nanowires or nanoribbons consisting of 1D atomic threads, i.e. chains. We have examined the current carrying capacity of ZrTe3 nanoribbons using a set of structures fabricated by the shadow mask method. The bulk crystals were synthesized by the chemical vapor transport method and exfoliated onto Si/SiO<sub>2</sub> substrates. It was found that ZrTe<sub>3</sub> nanoribbons reveal an exceptionally high current density, on the order of ~100 MA/cm<sup>2</sup>, at the peak of the stressing DC current. The low-frequency noise was of 1/f type near room temperature (f is the frequency). The noise amplitude scaled with the resistance, following the trend established for other low-dimensional materials. The high current density in ZrTe<sub>3</sub> can be attributed to the single-crystal nature of quasi-1D van der Waals materials.

*Key Words* — van der Waals materials; quasi-1D materials; nanowires; interconnects; current density; ZrTe<sub>3</sub>

## I. INTRODUCTION

As aggressive scaling in the complementary metal-oxide semiconductor (CMOS) technology continues, there is a growing need to examine new materials that can be used for nanometer-scale local interconnects or device channels [1], [2]. More than a decade ago, the industry moved from aluminum (Al) to copper (Cu) to lower resistance and improve reliability via reduced electromigration. Recently, it has been reported that the 10-nm logic technology node features local interconnects made of cobalt (Co), which enables a 10-fold reduction in electromigration [2]. At present, the current density sustained by Cu interconnects in state-of-the-art CMOS technology is between 2 MA/cm<sup>2</sup> to 3 MA/cm<sup>2</sup> [3], [4].

We recently proposed that quasi-one-dimensional (1D) van der Waals materials, such as transition metal trichalcogenides (TMTs) with formula  $MX_3$  (where M = Nb, Ta, Ti, Zr, and other

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A. A. Balandin, A. Geremew, E. Aytan, G. Liu and S. Rumyantsev are with the Department of Electrical and Computer Engineering, University of California, Riverside, California 92521 USA (<sup>\*</sup>corresponding author e-mail: <u>balandin@ece.ucr.edu</u>; web-site: <u>http://balandingroup.ucr.edu/</u>). M. P. Rao, B. W. K. Woo and S. R. Corber are with the Department of Mechanical Engineering, University of California, Riverside, California 92521 USA. K. Bozhilov is with the Materials Science and Engineering Program, University of California, Riverside, California 92521 USA. M. A. Bloodgood and T. T. Salguero are with the Department of Chemistry, University of Georgia, Athens, Georgia 30602, USA. transition metals; X = S, Se, Te), have properties attractive for applications in nm-scale electronics. In a way similar to transition metal dichalcogenides (TMDs), which exfoliate into 2D layers [5], [6], TMTs exfoliate into quasi-1D atomic thread bundles [7]–[9]. We previously reported that metallic TaSe<sub>3</sub> nanowires have breakdown current density, at the peak of the stressing DC current, on the order of ~10 MA/cm<sup>2</sup> [7], [8]. In principle, such quasi-1D materials could be ultimately downscaled by exfoliation, or grown directly, into nanowires with a cross-section of ~1 nm × 1 nm, which corresponds to an individual atomic thread, *i.e.* chain.

Here we show that nanoribbons made of ZrTe<sub>3</sub>, another member of the TMT family, reveal exceptionally high current density, on the order of ~100 MA/cm<sup>2</sup>, at the peak of the stressing DC current (with a cross-section of ~27 nm × 450 nm). Our experimental data also indicate that the low-frequency noise (LFN), another important metric of the material's applicability in electronics, is relatively low, and scales down with the resistance, following the trend found for some other low-dimensional materials.

### II. MATERIAL SYNTHESIS AND CHARACTERIZATION

The selection of  $ZrTe_3$  was based on reports indicating that  $ZrTe_3$  has better electrical conduction properties than other MX<sub>3</sub> materials [10]. For this study,  $ZrTe_3$  crystals were synthesized by the chemical vapor transport (CVT) method [7], [8], [11], [12]. ZrTe\_3 crystallizes in the monoclinic space group



**Figure 1:** Raman spectra of  $ZrTe_3$  for CVT bulk (solid blue) and reference bulk (solid red). The data for exfoliated nanoribbons are shown by dashed lines of the corresponding colors. All spectra were recorded under 633 nm laser excitation and power below 1.5-mW on the surface, to avoid local heating. Inset shows TEM image of the CVT grown exfoliated  $ZrTe_3$  crystal. The scale bar is 2  $\mu$ m.

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 $P2_1/m$ . It has strongly anisotropic electronic transport properties, and its bulk electrical resistivity is lower than that of TaSe<sub>3</sub> and several other MX<sub>3</sub> [13]–[16]. However, there are inconsistencies in the literature with respect to the reported electronic band structure of ZrTe<sub>3</sub>, *i.e.* metallic *vs.* semiconducting, type of conduction, as well as in the transition temperatures to the charge density wave and superconducting phases [10], [13]–[18]. These issues are not directly related to the present study, which is focused on the current carrying capacity of ZrTe<sub>3</sub> nanoribbons.

The CVT-grown crystals were characterized by Raman spectroscopy, X-ray diffraction (XRD), scanning electron microscopy (SEM), energy dispersive spectroscopy (EDS), and high-resolution transmission electron microscopy (HR-TEM). Figure 1 shows Raman spectra (Renishaw) of bulk ZrTe<sub>3</sub> and exfoliated ZrTe<sub>3</sub> threads under 633 nm laser excitation for the CVT-grown material and a reference commercial sample (HQ Graphene). All spectra are consistent and in agreement with the literature [19], [20]. As expected for  $P2_1/m$  symmetry, ZrTe<sub>3</sub> exhibits five main peaks in the range from 120 cm<sup>-1</sup> to 250 cm<sup>-1</sup>.

Mechanical exfoliation onto a Si/SiO<sub>2</sub> substrate was used to obtain ZrTe<sub>3</sub> nanoribbons. One should note that ZrTe<sub>3</sub> occupies an intermediate position between quasi-2D and quasi-1D materials, owing to its structure and bonding characteristics [13]–[16]. For this reason, it exfoliates into ribbon-like structures with widths larger than thicknesses; thus we refer to nanoribbons of ZrTe<sub>3</sub> vs. nanowires of TaSe<sub>3</sub>. The length of the exfoliated nanoribbons could reach hundreds of  $\mu$ m. While exfoliation can often yield nanoribbons with few-nm lateral dimensions, we focused on the thickness range  $\leq$ 50 nm and the width range  $\leq$ 500 nm, in order to simplify device fabrication and to ensure the quality of the metal contacts.

### III. DEVICE FABRICATION

We utilized the shadow mask method to directly deposit transmission line measurement (TLM) structures onto preselected ZrTe<sub>3</sub> nanoribbons. The exfoliated ZrTe<sub>3</sub> nanoribbons were relatively stable in the air (e.g. compared to TaSe<sub>3</sub>), which allowed us to use the shadow mask method. This method was used because it avoids the damage and chemical contamination typically associated with conventional lithographic lift-off processes. It also drastically reduces the total air exposure time. The shadow masks were fabricated using double-side polished Si wafers with 3 µm thermally grown SiO<sub>2</sub> (Ultrasil Corp.; 500µm thickness; P-type; <100>). The shadow mask fabrication process began with evaporation of 200 nm chromium (Cr) on the front side of the wafer (200-nm thickness), followed by a combination of electron beam lithography (EBL) and Cr etchant (1020A) to create a stencil of the TLM pattern. This was followed by fluorine-based reactive ion etching (RIE) to transfer the pattern to the underlying SiO<sub>2</sub>. Finally, the pattern was transferred into the underlying Si substrate using the deep reactive ion etching (DRIE) (Oxford Cobra). The DRIE etch step was timed to break through to a large backside window that was previously defined using lithographic patterning, RIE, and DRIE. The shadow masks were used to fabricate ZrTe<sub>3</sub> devices by aligning them with pre-selected nanoribbons on the device substrate, clamping the aligned mask and device substrate

together, and placing the clamped assembly in an electron beam evaporator (EBE) for contact deposition (10 nm Ti and 100 nm Au) through the shadow mask openings. The completed devices were then transferred to another vacuum chamber for electrical characterization. The thickness and width of ZeTe<sub>3</sub> nanoribbon were determined from the atomic force microscopy (AFM) inspection. Figure 2 shows scanning electron microscopy (SEM) images of representative Si shadow masks, as well as

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**Figure 2:** (a) SEM image of a shadow mask with TLM stencil structure patterned on 500- $\mu$ m thick Si/SiO<sub>2</sub> wafer. (B) Top-view SEM image of the pattern for Ti and Au evaporation to create the source and drain contacts. (C) AFM image of the quasi-1D ZrTe<sub>3</sub> nanoribbon device, fabricated using the shadow mask. AFM characterization was used to determine the nanowire width and thickness (~ 33-nm in the present case). (D) SEM image of another quasi-1D ZrTe<sub>3</sub> nanowire device with a different cross-sectional area. The scale bars in (a), (b) and (d) are 50  $\mu$ m, 2  $\mu$ m and 1  $\mu$ m respectively.

AFM and SEM images of the ZrTe<sub>3</sub> nanoribbon devices fabricated with the masks.

#### IV. EXPERIMENTAL RESULTS AND DISCUSSION

The current-voltage (I–V) characteristics were measured using a probe station (Lakeshore) and a semiconductor analyzer (Agilent B1500). The low-field I-V characteristics of the devices were linear, indicating the Ohmic nature of the contacts and ZrTe<sub>3</sub> channels (see inset to Figure 3). The extracted contact resistance for typical devices was  $2R_C \approx 18 \Omega$ , *i.e.* less than 3% of the total ZrTe<sub>3</sub> channel resistance. In Figure 3, we present I-V characteristics of a representative quasi-1D ZrTe<sub>3</sub> nanoribbon device, which was biased up to its complete breakdown. In these devices, the peak of the stressing DC current reached ~15.8 mA at the bias voltage of ~1.6 V. This corresponds to a maximum breakdown current density on the order of 100 MA/cm<sup>2</sup>. Four other tested devices revealed similar current densities. The electrical resistivity did not show a clear scaling trend, fluctuating in the range from  $\sim 3 \times 10^{-4} \Omega$ cm to  $\sim 7 \times 10^{-4}$  Ω-cm. The obtained values are in line with the data reported in literature for bulk crystals [14], [16]. One should note that there have been reports suggesting that ZrTe<sub>3</sub> can crystalize in two polymorphs, which have either metallic or semiconducting behavior [17]. However, conditions for phase pure growth have not yet been established, in part because the structural differences between the proposed polymorphs are subtle, making the polymorphs difficult to distinguish by

typical characterization techniques (*e.g.*, powder XRD). It is likely that CVT conditions yield a mixture of polymorphs, and thus one can expect a variation in resistivity depending on the dominant phase of the atomic threads.

The breakdown current density obtained for ZrTe<sub>3</sub> is a factor  $\times 10$  larger than the current density at the maximum stressing current measured for Cu wires, tested before Cu introduction in the interconnect technology [21]. It is also a factor of  $\times 3$  larger than the current density at the maximum stressing current for the best TaSe<sub>3</sub> nanowire devices reported to date [7]. Overall, the current density achieved in ZrTe<sub>3</sub> nanoribbons is extremely high. This can be attributed to the specific single crystalline structure of quasi-1D van der Waals materials, which minimizes electron scattering at grain boundaries and by interface dangling bonds. Measurements of the low-frequency noise (LFN) are often used to assess the material quality and reliability in conventional devices [22], [23] as well as in 2D materials and devices [24], [25], [26]. Figure 4 shows the noise characteristics of quasi-1D ZrTe3 nanoribbons measured at room temperature. LFN is of 1/f type (f is the frequency). The noise spectral density  $S_{I}/I^{2}\approx 10^{-8}$  -  $10^{-7}$  Hz<sup>-1</sup> at f=1 Hz is comparable to that in some other low-dimensional materials. It scales with resistance, R, as ~ $10^{-11} \times R$ , following the trend observed for carbon nanotubes, graphene and metallic nanowires [8]. The noise level can be expected to decrease further with improved material quality.



**Figure 3:** High field I-V characteristics of the best quasi-1D ZrTe<sub>3</sub> nanoribbon device. The apparent breakdown current density, calculated with the AFM measured thickness and SEM measured width, corresponds to ~10<sup>8</sup> A/cm<sup>2</sup>, reached at the voltage bias of ~1.6 V (with a nanoribbon cross-section of ~27 nm × 450 nm). The current shows some signs of instability at V≈1.2 V indicating that some atomic threads started to break. The inset shows low-field I-V characteristics of quasi-1D ZrTe<sub>3</sub> devices with different channel lengths used for the contact resistance extraction. The data indicate the Ohmic nature of the contacts and channel.

#### V. CONCLUSIONS

We investigated the current carrying capacity of nanoribbons made from ZrTe<sub>3</sub>, a quasi-1D van der Waals material. The nanoribbon devices revealed an exceptionally high current density, on the order of ~100 MA/cm<sup>2</sup>, at the peak of the stressing DC current. Our results suggest that quasi-1D van der Waals materials that consist of single-crystals composed of  $MX_3$  atomic threads have potential for applications in future downscaled electronic technologies.

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Figure 4: Normalized noise spectrum density as a function of frequency for  $ZrTe_3$  nanoribbon devices with a cross-section ranging from ~27 nm × 450 nm to ~100 nm × 2000 nm. The inset shows AFM image of the fabricated structure with the devices.

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